

FIG. 1

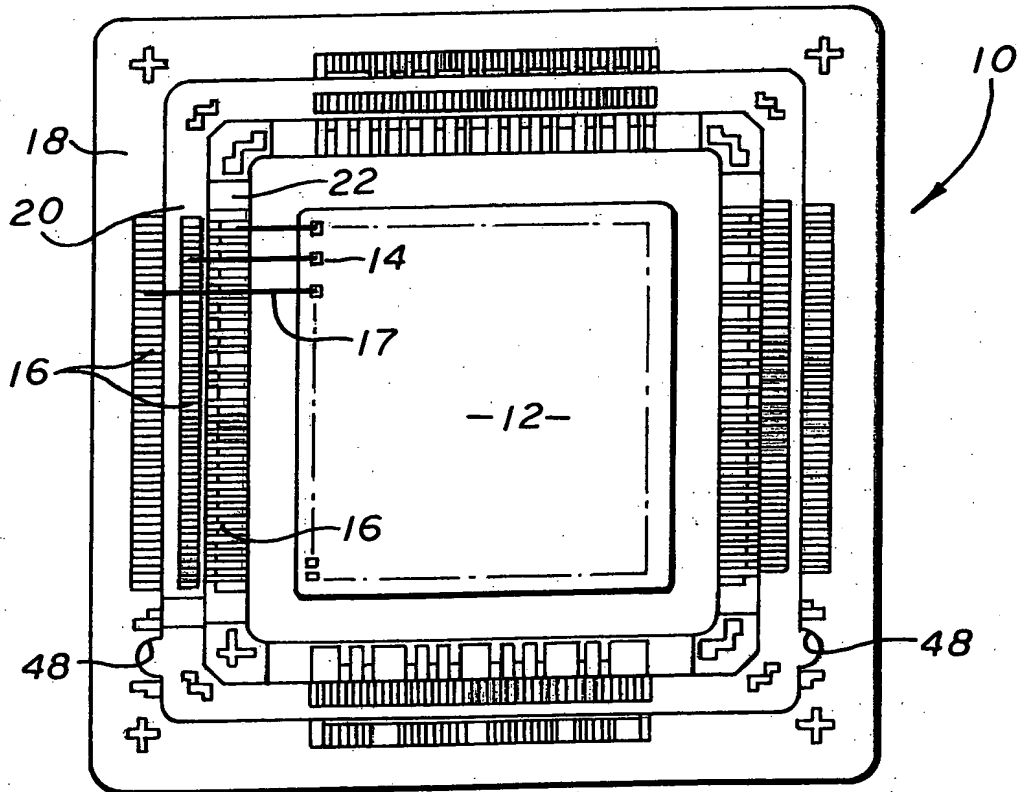


FIG. 2



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Title: METHODS FORMING AN INTEGRATED CIRCUIT PACKAGE WITH A SPLIT CAVITY WALL

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Application No.: 09/665,034

Docket No.: 42390P6139C

Sheet: 2 of 3

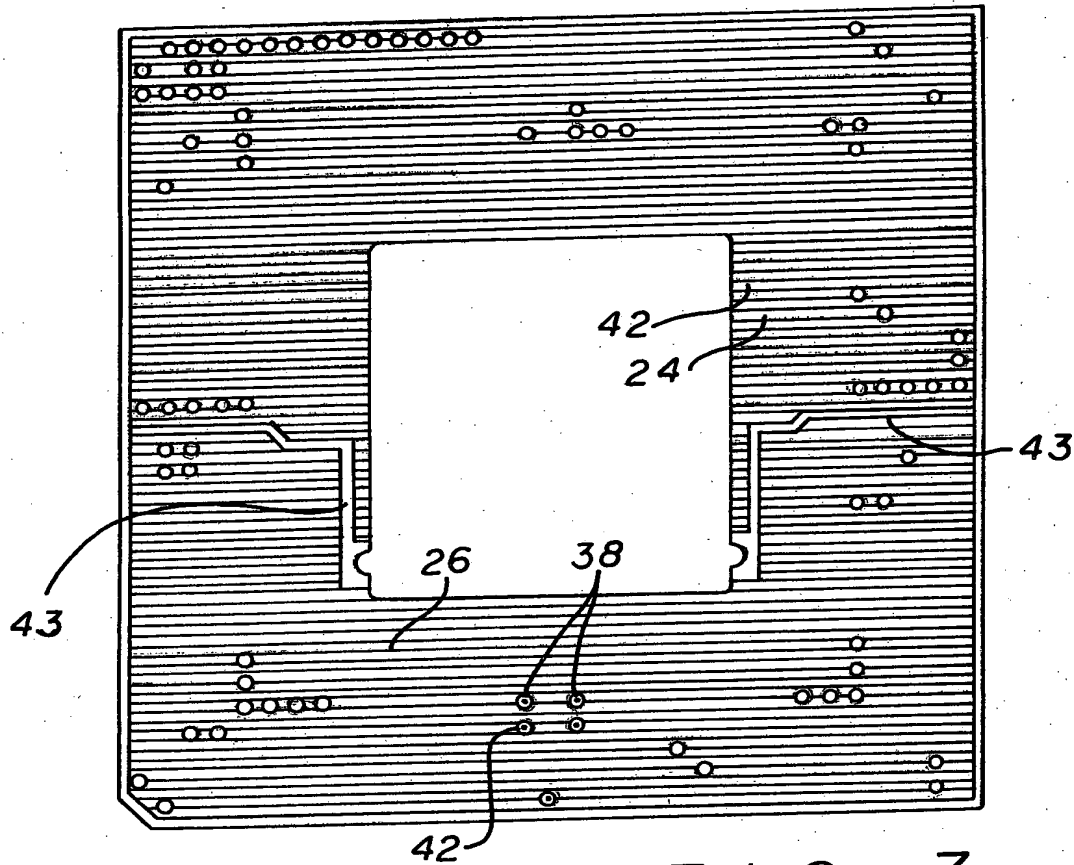


FIG. 3

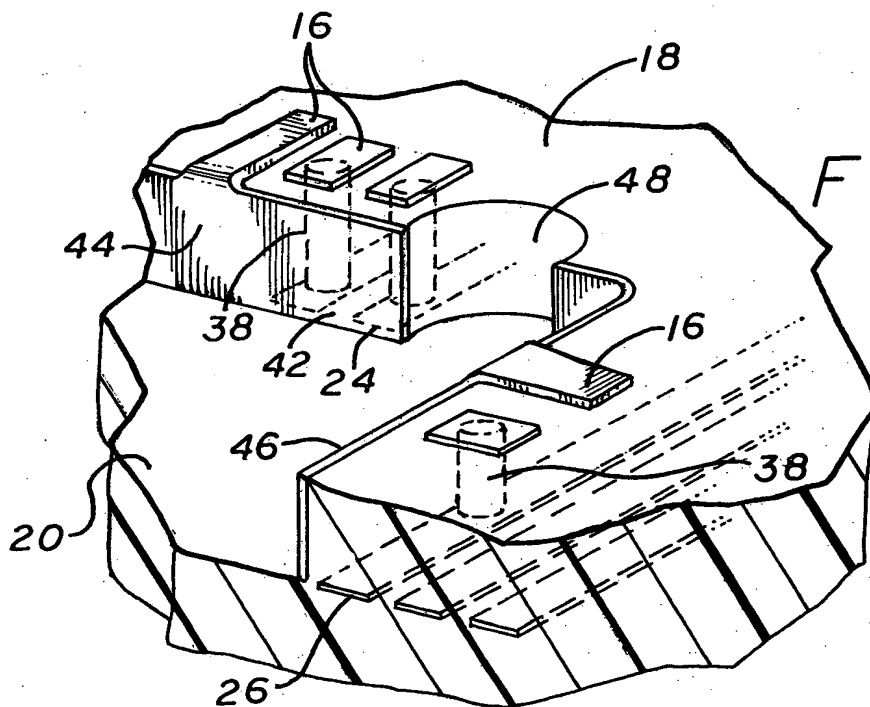
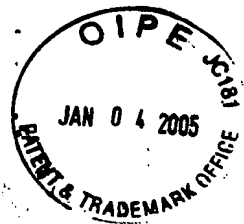


FIG. 4



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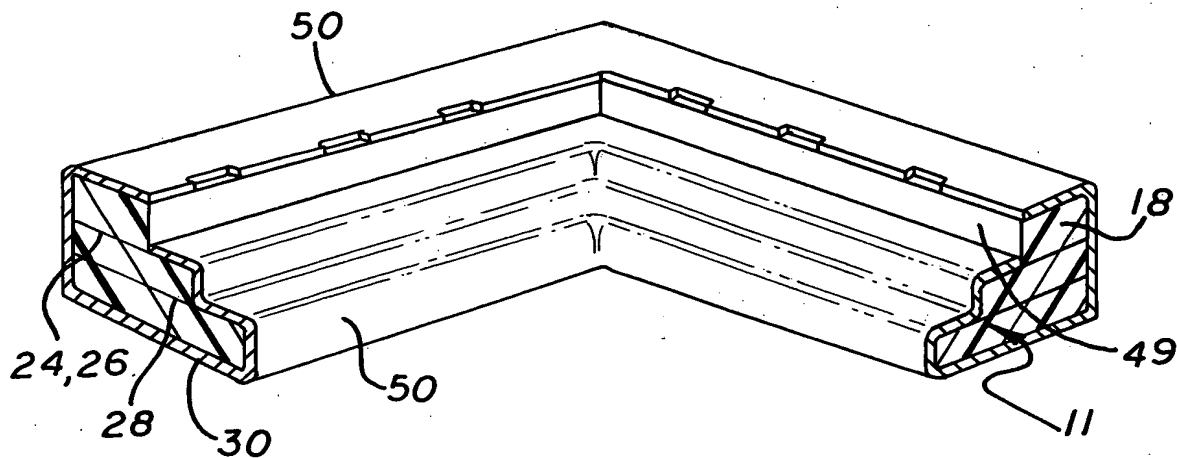


FIG. 5

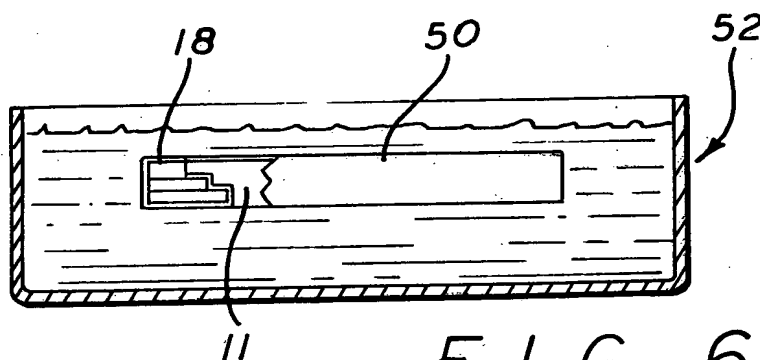


FIG. 6

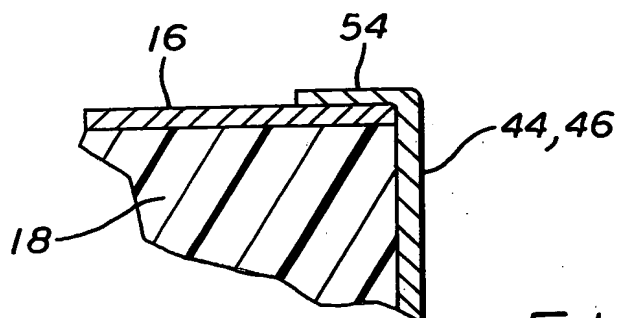


FIG. 7